Exam : **HP0-236**

Title : Supporting SAN
Infrastructure & Solutions

Version : DEMO

1.What should be the maximum number of active storage controller ports per inter-switch link in a high throughput computing environment?

A.2

B.4

C.10

D.20

Correct:D

2.Which factor can reduce the performance between devices in a Fibre Channel SAN?

A.hop count

B.switch latency

C.inter-switch link latency

D.inter-switch link congestion

Correct:D

3.Which ports should you use on a B-series switch to establish two Wavelength Division Multiplexing links to a remote site?

A.ports which belong to the same "Quad"

B.ports which belong to different "Quads"

C.ports on different switches

D.only 2 Gbps ports

Correct:B

4.What do you recommend if a performance bottleneck is discovered on a link between a 1 Gbps and a 2 Gbps B-series switch?

A.Implement trunking on this link.

B.Implement port channeling on this link.

C.Use multi-mode cable instead of single mode.

D.Replace the 1 Gbps switch with a 2 Gbps switch.

Correct:D

5.A complex SAN designed for applications with small IOs is used primarily by applications with large IOs. What is the impact to the SAN?

A.The supported hop count is reduced.

B.There may be congestion on inter-switch links.

C.None. SAN design is not dependent on application IO size.

D.The buffer-to-buffer credit on the storage system must be recalculated.

Correct:B

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